

PATENT

Atty. Dkt. No. ATT 1999-0177

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claims 1-2 ( Canceled)

3. (Currently amended) ~~The method of claim 1, further including a step of~~ A method of switching a signal comprising:

coupling an input to output by plural stages of switching;

processing the signal switching by coding techniques in one stage of the plural stages of switching;

processing the signal switching by timing techniques in another stage of the plural stages of switching;

coordinating the coding techniques and timing techniques to couple a signal at an input port to a predetermined output port;

despreading a code division signal; [[:]]

time multiplexing the despread code division signal; and

respreading the time multiplexed signal.

4. (Currently amended) ~~The method of claim 1, further including a step of~~ A method of switching a signal comprising:

coupling an input to output by plural stages of switching;

processing the signal switching by coding techniques in one stage of the plural stages of switching;

processing the signal switching by timing techniques in another stage of the plural stages of switching;

coordinating the coding techniques and timing techniques to couple a signal at an input port to a predetermined output port;

spread coding a time multiplexed signal;

code division switching the spread coded signal; and

time multiplexing the code division switched signal.

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5. (Currently amended) ~~The method of claim 2, further including a step of~~ A method of switching a signal comprising:

coupling an input to output by plural stages of switching;

processing the signal switching by coding techniques in one stage of the plural stages of switching, wherein the processing in one stage comprises:

switching by timing techniques comprises a time slot interchange step;

and

switching by coding techniques comprises a code division step;

processing the signal switching by timing techniques in another stage of the plural stages of switching;

coordinating the coding techniques and timing techniques to couple a signal at an input port to a predetermined output port;

combining signals into beams and encoding the beams with overspreading techniques to identify a destination; and

coding individual signals to identify a particular user.

6. (Original) A time division code switch, comprising:

an input for accepting a code division modulated signal and including an input despreader to decode the code division modulated signal;

a time slot interchanger connected to receive the decoded code division modulated signal, the time slot interchanger re-arranging the time slots of each frame to couple input to a designated output;

respreading circuitry connected to an output of the time slot interchanger; and

an output for directing respread signals to their destination.

7. (Original) The time division code switch of claim 6, comprising:

a code bus for delivering CDMA beams to a plurality of despreaders at the input;

and

a summer connected to sum a plurality of spread signals having a common destination to form a beam directed to that destination.

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Claims 8-12 (Canceled)